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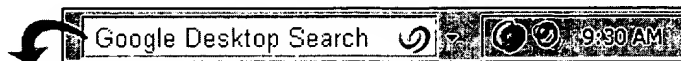
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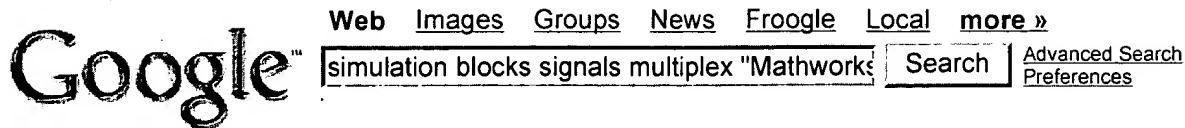
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introduction of a new bus block, from which all the vectorised **signals** can be referenced ... **blocks** are essentially the same as in the off-line **simulation**. ...

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3.2 An overview of SDF stars

You can get periodic **signals** with any period, and can halt a **simulation** at the end of ... The following **blocks** also appear in the signal processing palette. ...

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... and the time steps in the **SIMULATION PARAMETERS**. ... mux) and demultiplex (demux) **signals** to create "wider" **signals**. ... for us, there is a Simulink block for a ...

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Since the coded_block_pattern in NON-INTRA macroblocks **signals** every possible

... Since EOB is required for all coded **blocks**, its absence can signal that a ...

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... The final MPEG-1 **simulation** model is version 3 ... of the DCT for decorrelation of prediction error **blocks**. ... the statistical correlation of intra **signals**, but less ...
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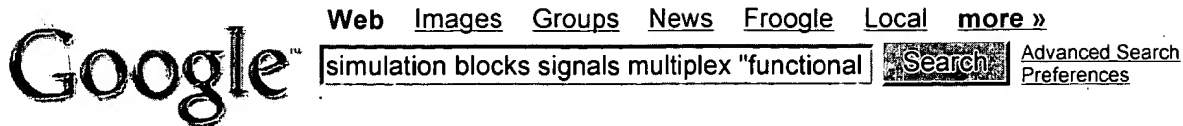
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commands. Figure 1.1 Principal **functional blocks** of a switching power converter

... The PWM Generator block produces PWM **signals** for the node gate drives. ...

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... network The through-connection of **multiplex signals** via cross connectors ...

by signal bundle means CAD system placing **functional blocks** with a circuit ...

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... Control **Signals** ... It was estimated that the synthesis phase including iterative **simulation**

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... 7.5.2 Human or Machine Peripheral system block ... Two approaches are subsequently developed:

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preset **signals** to the WCD and RCD **blocks**, thus ensuring a >10-bit ... Initially,

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In this case, the **functional blocks** describe the behavior of architectural elements

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... Multiple-Output Current-Mode **Blocks** for Linear ... procesu výroby integrovaných obvodu

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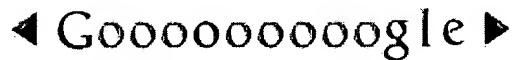
In this chapter, main **functional blocks** of the inverter system are ... IGBT power switches are selected based on the **simulation** or analysis with ...

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Terms and Definitions

The combination of the command, acknowledge, and data **signals** is called a bus ... The act of moving data inside the computer in **blocks** referred to as pages. ...

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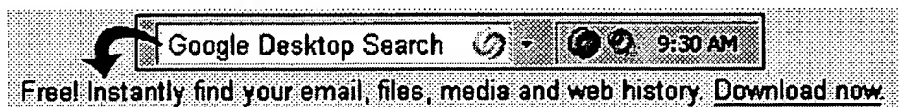
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ISBN: 1-56555-142-7.

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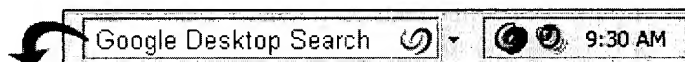
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Patent Assignment Abstract of Title

Total Assignments: 1**Application #:** 09870280 **Filing Dt:** 05/30/2001**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** US20020183998**Pub Dt:** 12/05/2002**Inventors:** Mojdeh Shakeri, Marc Ullman, Ramamurthy Mani**Title:** Block diagram modeling**Assignment: 1****Reel/Frame:** 011863/0939**Received:**
06/08/2001**Recorded:**
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5**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** SHAKERI, MOJDEH**Exec Dt:** 05/22/2001ULLMAN, MARC**Exec Dt:** 05/24/2001MANI, RAMAMURTHY**Exec Dt:** 05/22/2001**Assignee:** MATHWORKS, INC., THE

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